



# IPC-TM-650 TEST METHODS MANUAL

Number <b>2.5.16</b>	
Subject <b>Shorts, Internal on Multilayer Printed Wiring</b>	
Date <b>11/88</b>	Revision <b>A</b>
Originating Task Group <b>Electrical Continuity Testing Task Group (7-32c)</b>	

**1.0 Scope** This test method is to detect internal electrical short circuits within multilayer printed wiring boards.

**2.0 Applicable Documents** None

**3.0 Test Specimen** Test coupon "B" on test pattern described in section 5.8.4 of this publication, or any production boards.

**4.1 Power Supply** Power supply capable of providing 100 volts DC.

**4.2 Meter** Electrical ohmmeter capable of measuring resistances between conductors.

## 5.0 Procedure

### 5.1 Test

**5.1.1** Perform test at ambient temperature.

**5.1.2** Apply a polarized voltage of 100 volts DC between a plated through hole connected to a ground plane and an interconnection of hole not connected to this plane

**5.1.3** Measure the resistance between these points and record findings.